



PTO/SB/08A (08-03)

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Application Number	10/804,182
Filing Date	March 19, 2004
First Named Inventor	Paul Demone
Art Unit	2818
Examiner Name	SON MAI
Attorney Docket Number	16319-220US-1

Sheet 1 of 3

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
JH		US- Re. 35,934	10-27-1998	Takai	
JH		US- 5,666,480	09-09-1997	Leung et al.	
JH		US- 6,539,454 B2	03-25-2003	Mes	
JH		US- 5,402,388	03-28-1995	Wojcicki et al.	
JH		US- 5,544,124	08-06-1996	Zagar et al.	
JH		US- 5,550,784	08-27-1996	Takai	
JH		US- 5,579,267	11-26-1996	Koshikawa	
JH		US- 5,655,105	08-05-1997	McLaury	
JH		US- 5,703,815	12-30-1997	Kuhara et al.	
JH		US- 5,713,005	01-27-1998	Proebsting	
JH		US- 5,784,705	07-21-1998	Leung	
JH		US- 5,835,443	11-10-1998	Fujita	
JH		US- 6,014,339	01-11-2000	Kobayashi et al.	
JH		US- 6,711,083 B2	03-23-2004	Demone	
JH		US- 5,787,457	07-28-1998	Miller et al.	
JH		US- 5,856,940	01-05-1999	Rao	
JH		US- 6,151,236	11-21-2000	Bondurant et al.	
JH		US- 6,208,563 B1	03-27-2001	Naritake	
JH		US- 6,360,294 B1	03-19-2002	Ferrant et al.	

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ⁴
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				
JH		EP 0 280 882 A2	09-07-1988	Todd		
JH		EP 0 704 848 A2	03-04-1996	Koshikawa		
JH		JP 09091955	04-1997	X		

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Sheet 2 of 3

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Examiner Signature	SON MAI	Date Considered	08/25/04
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Sheet 3	of 3	Attorney Docket Number	16319-220US-1

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
JK		Boemo, Lopez-Buedo and Meneses, "The Wave Pipeline Effect on LUT-based FPGA Architectures", Ciudad Universitaria, Madrid, Spain	
JK		Mehrdad, Heshami et al., "A 250-MHz Skewed - Clock Pipelined Data Buffer", IEEE Journal of Solid-State Circuits, Vol. 31, No. 3, March 1, 1996, Pgs. 376-383	

Examiner Signature	SON MAI	Date Considered	08/25/04
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